


PROJECT NAME <b>i960 CPU Module</b>		 <b>DESIGN ANALYSIS ASSOCIATES, INC.</b> 75 West 100 South Logan, Utah 84321	
ENGINEER D. Witbeck	DATE 20 Jun 96	TITLE <b>i960 - FLASH &amp; UART WRITE</b>	
DRAWN D. Witbeck	20 Jun 96	DRAWING ID TD.vsd	HIERARCHY ID
CHECKED		PROJECT ID <b>i960</b>	REV <b>A</b>
APPROVED		SHEET	6 of 9
RELEASED			
SIZE <b>B</b>	LAST MODIFIED 30 Jan, 1997		